

Figure 1A
(Prior Art)

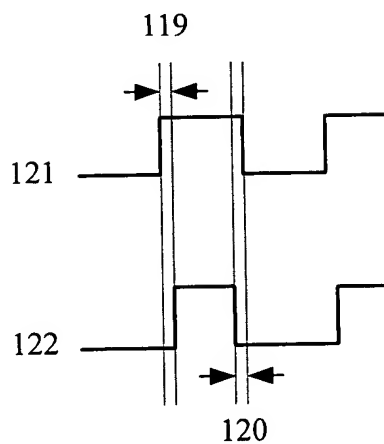


Figure 1B
(Prior Art)

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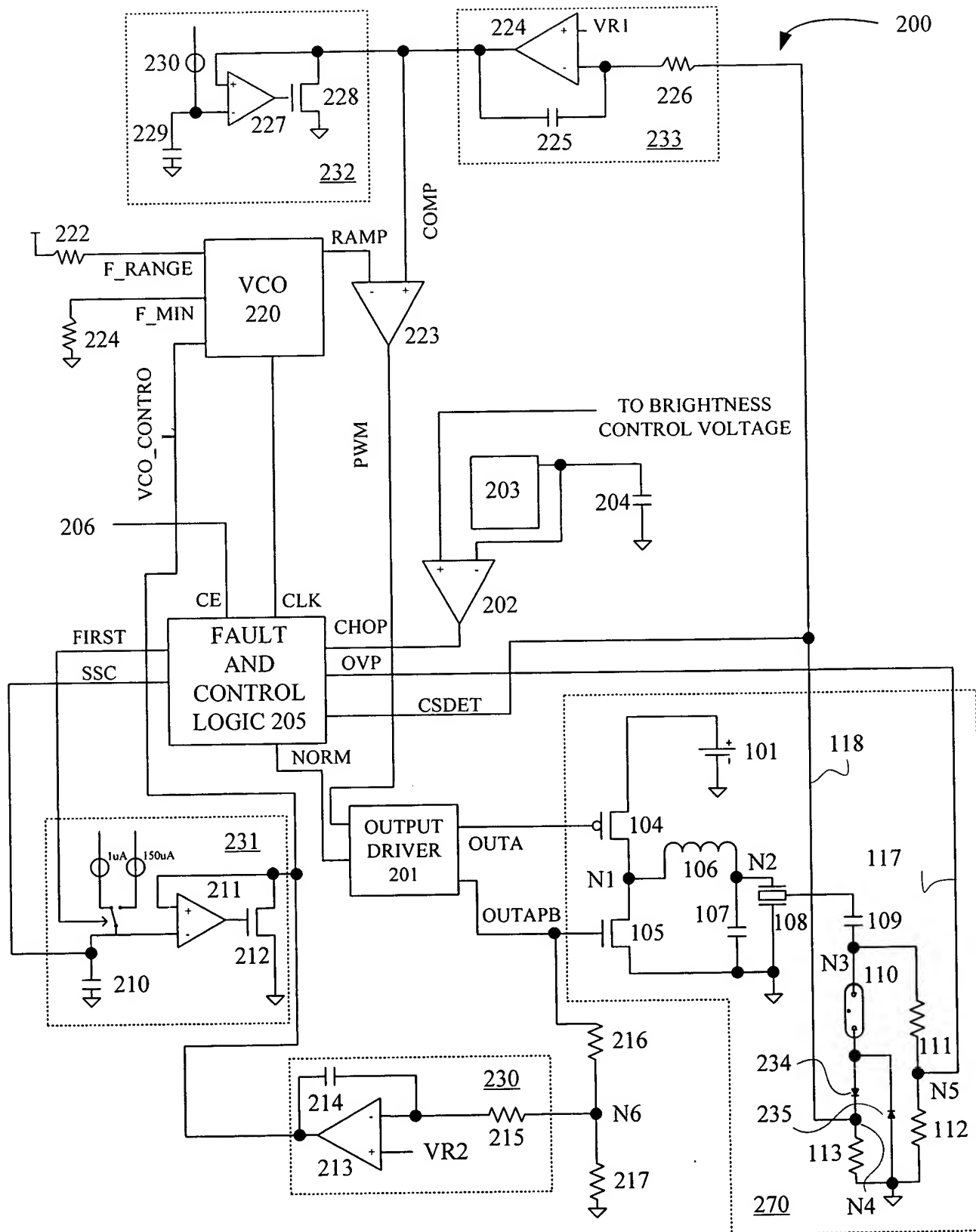


Figure 2A

Figure 2C is a detailed schematic of the control circuit 260. The circuit includes a 5.0V LDO, a 3.3V REF, and various op-amp stages (211, 223, 224, 202, 213). It also features a DAC (201) and a current source (203). The output stage (205) drives a load (204) and a feedback network (214, 215). The circuit is connected to a brightness control voltage and a current source (101).

Figure 2C

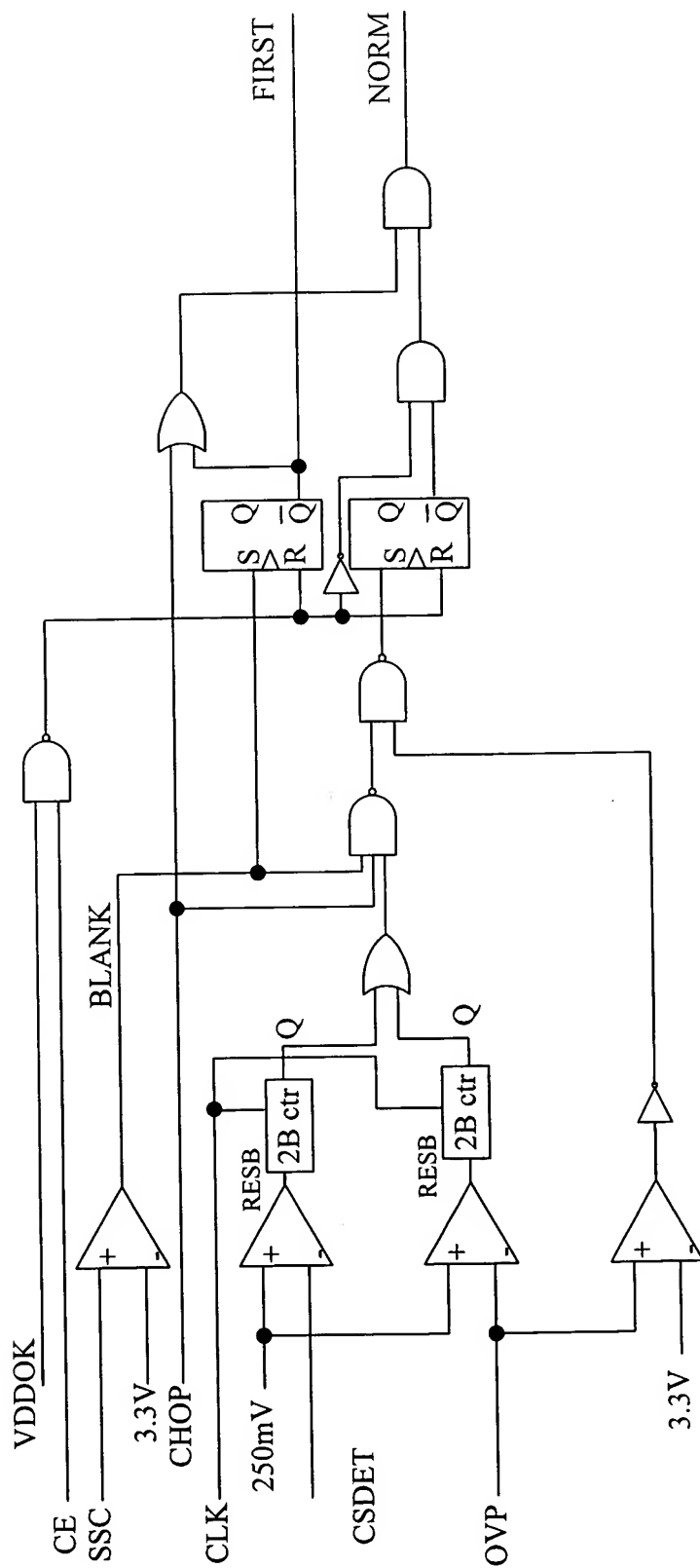


Figure 2E

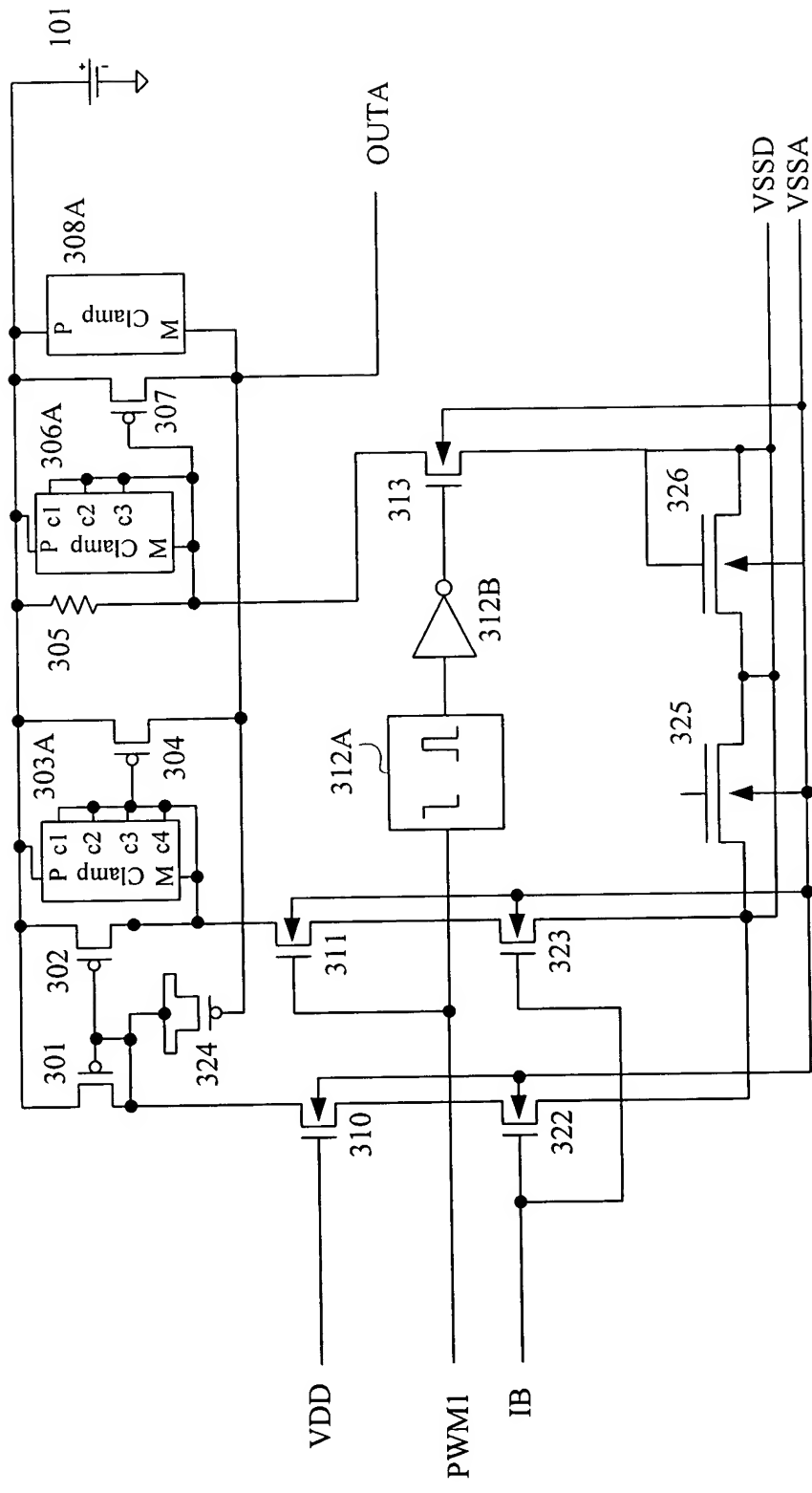


Figure 3B

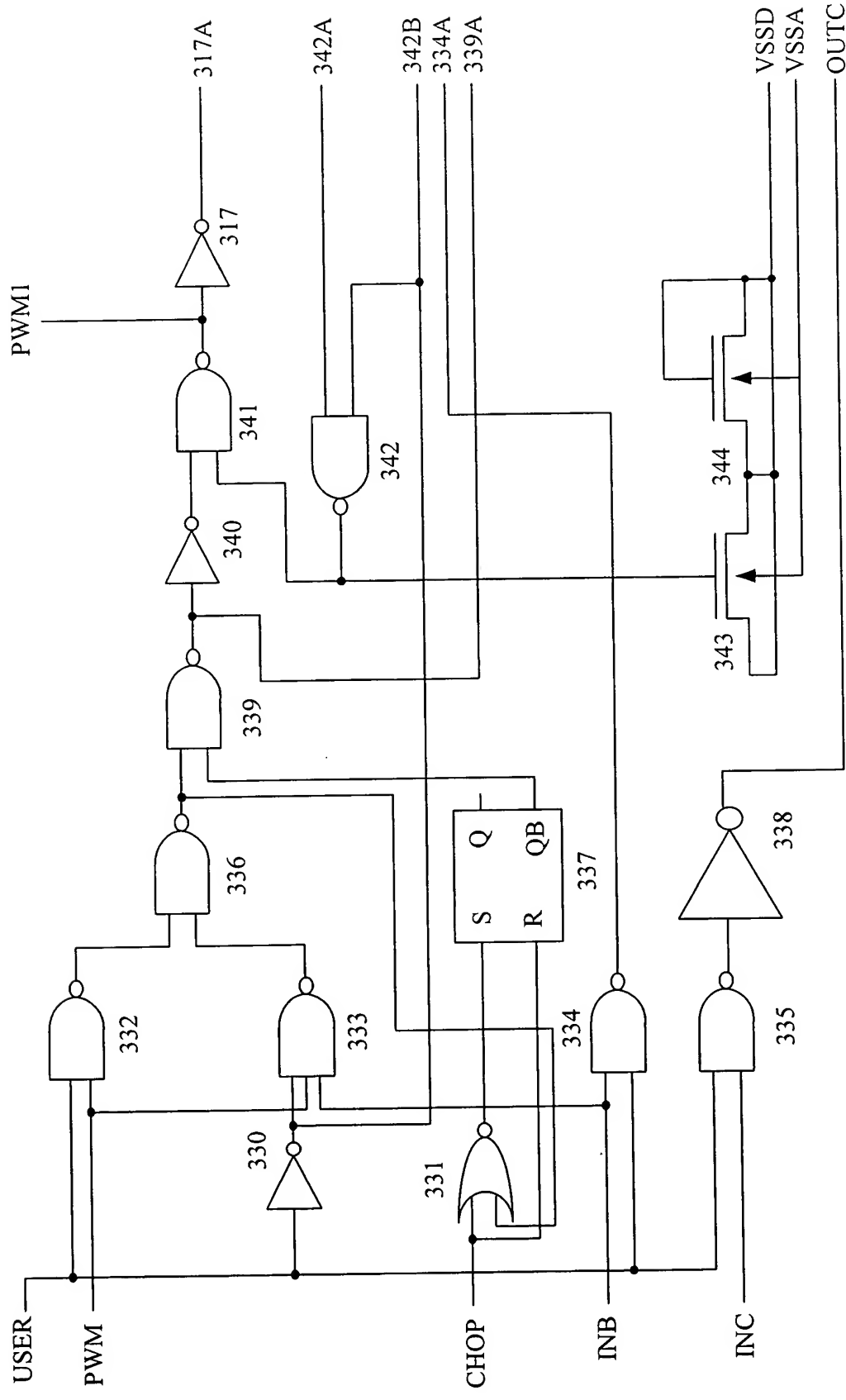
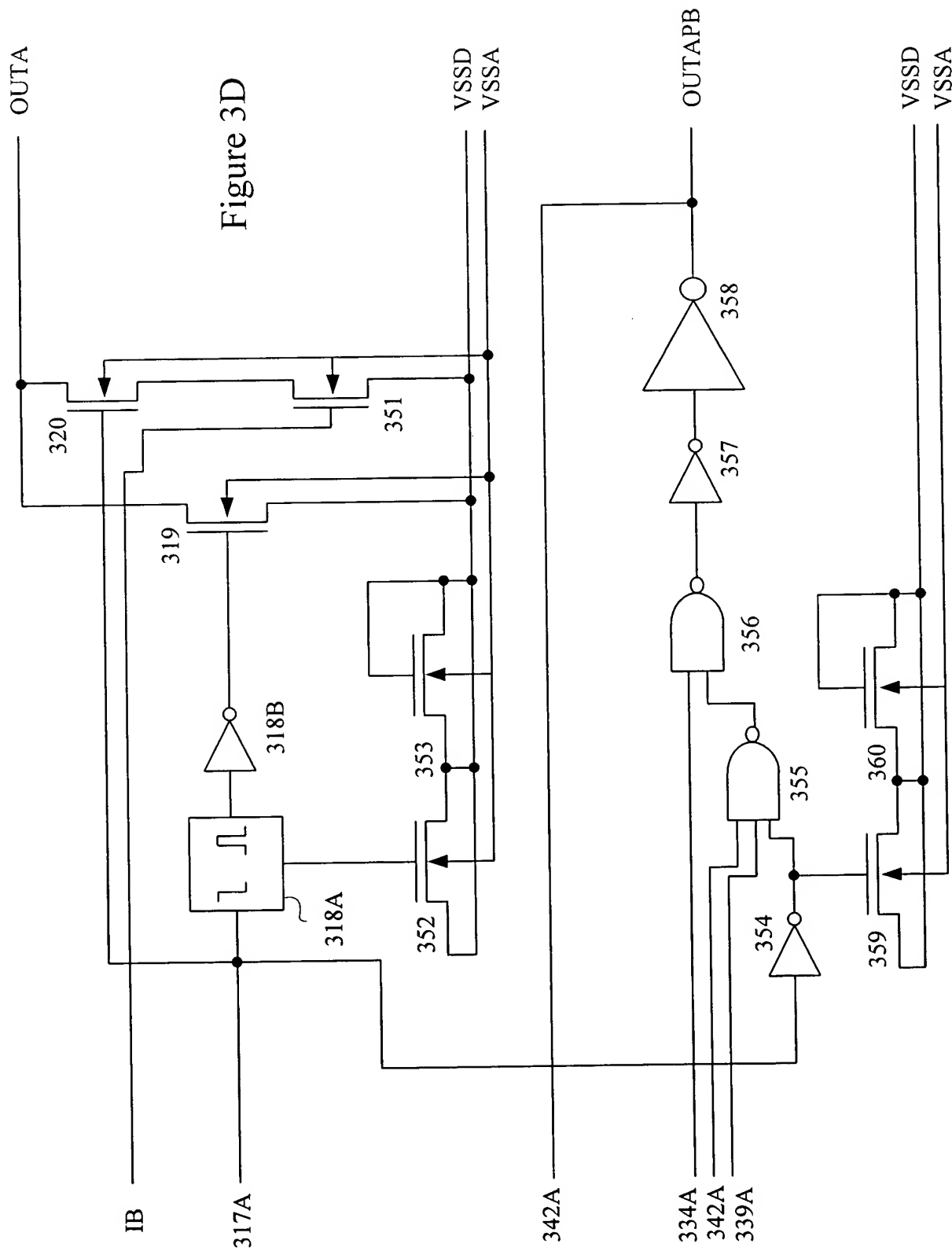


Figure 3C



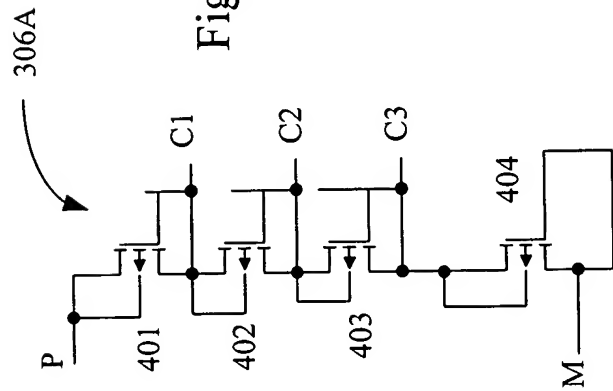


Figure 4B

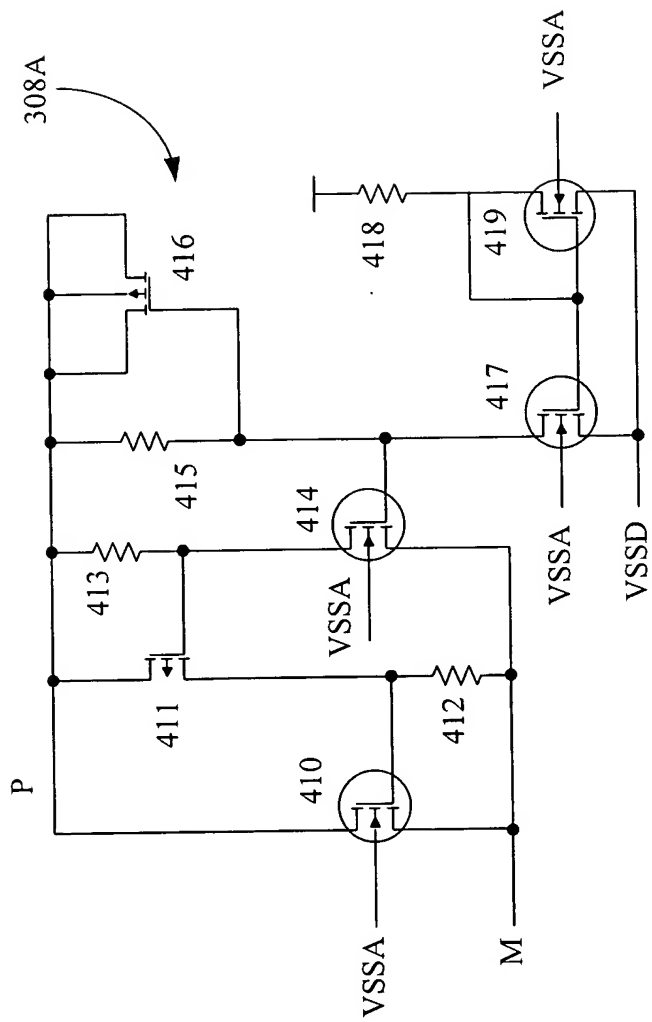


Figure 4C

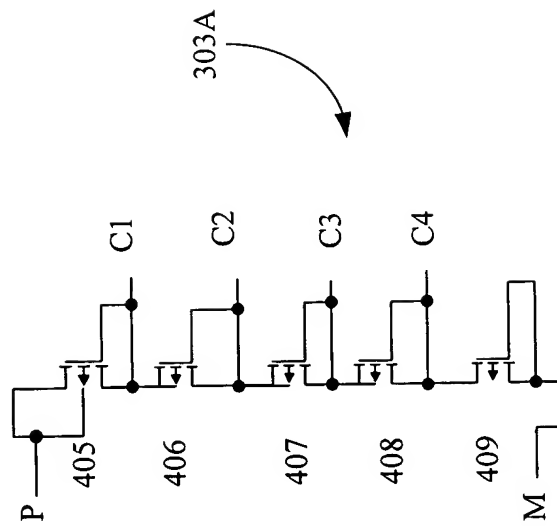


Figure 4A

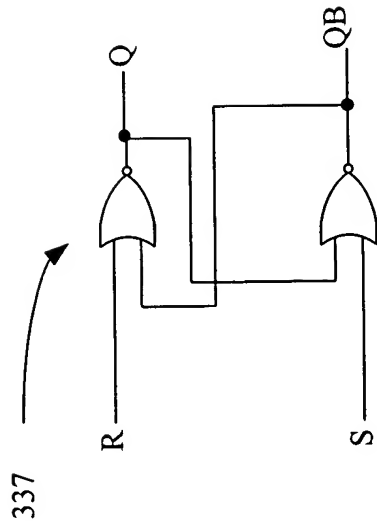


Figure 4D

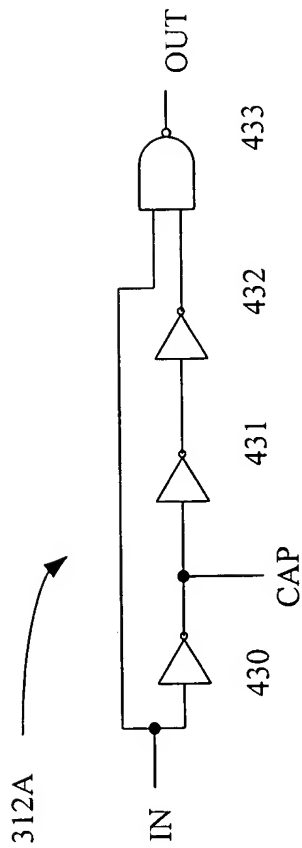


Figure 4E

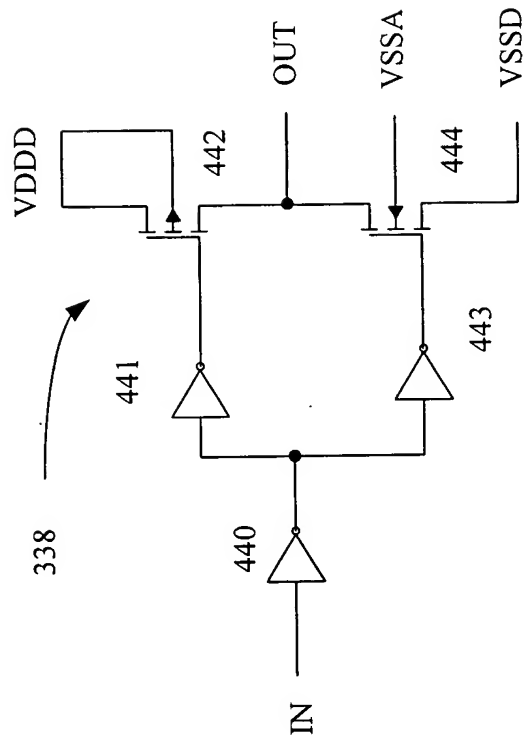


Figure 4F

209220-2E6E80T

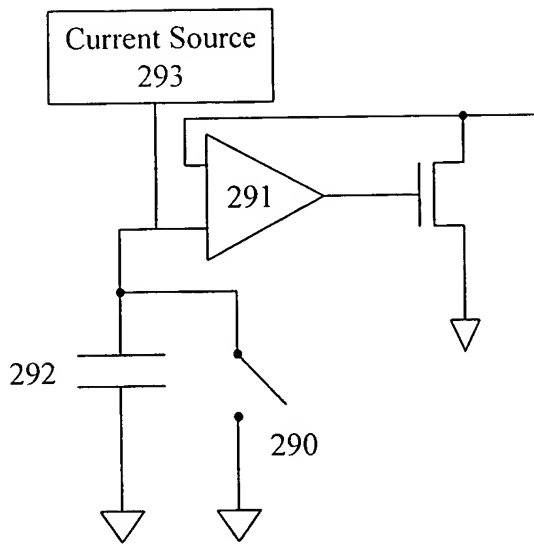


Figure 2B

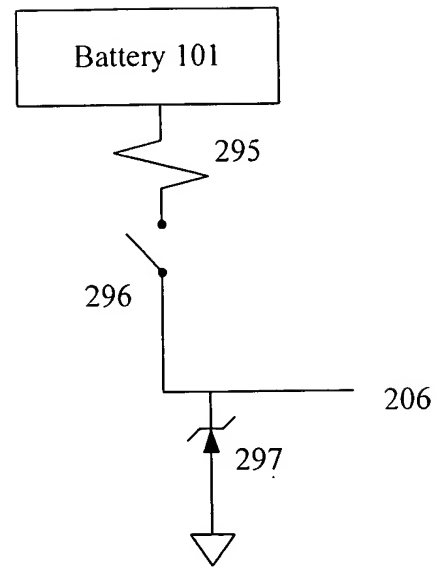


Figure 2D

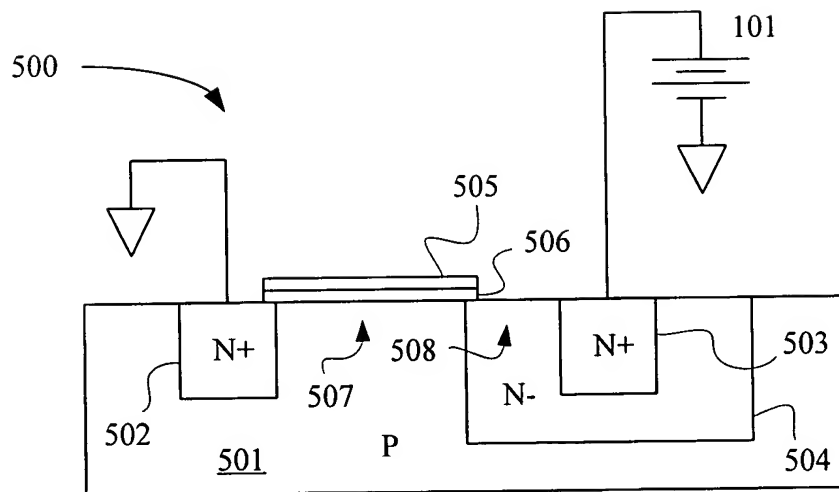


Figure 5